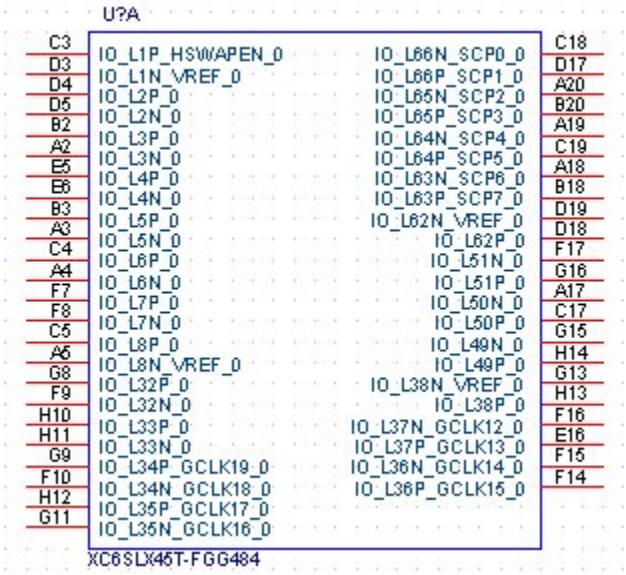
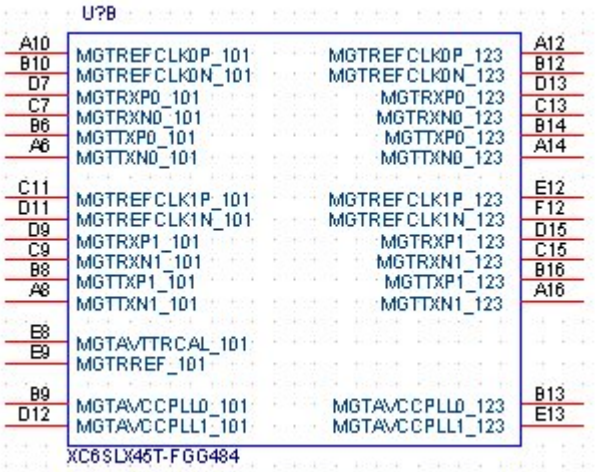
Schematic Symbol for XC6SLX45T-FGG484

The symbol consists of 7 heterogeneous parts, each of them listed below:

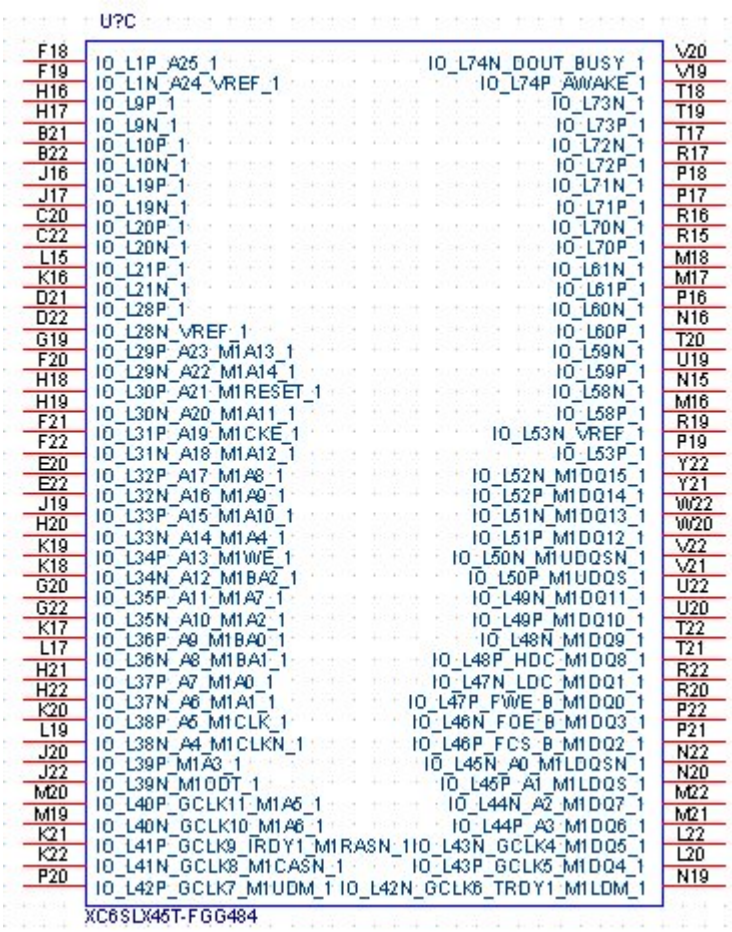
1. **I/O Bank 0**



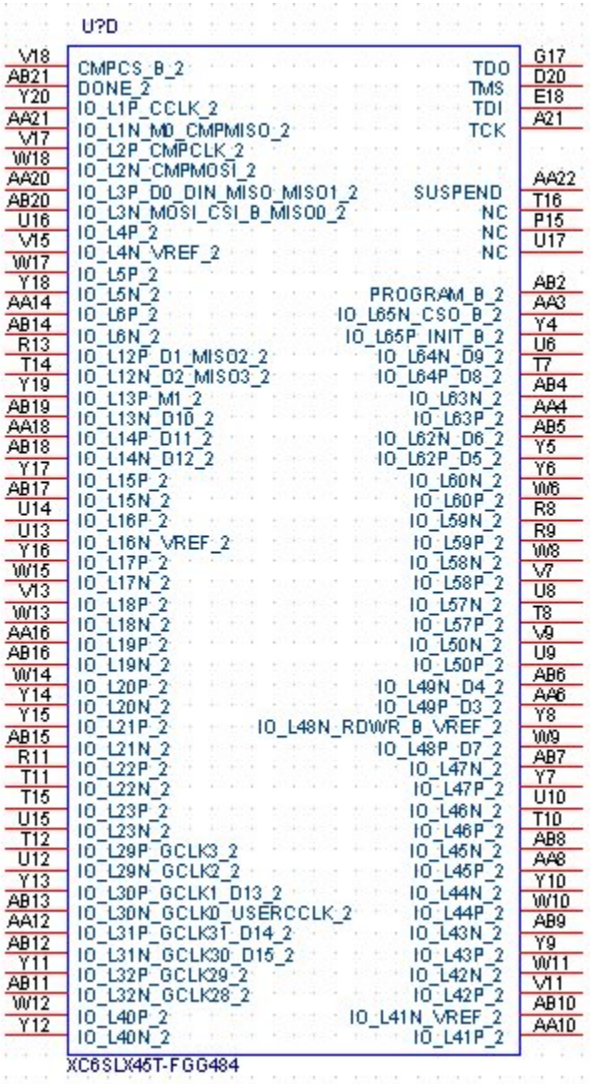
2. **MGT 101 and 123 (BANK0)**



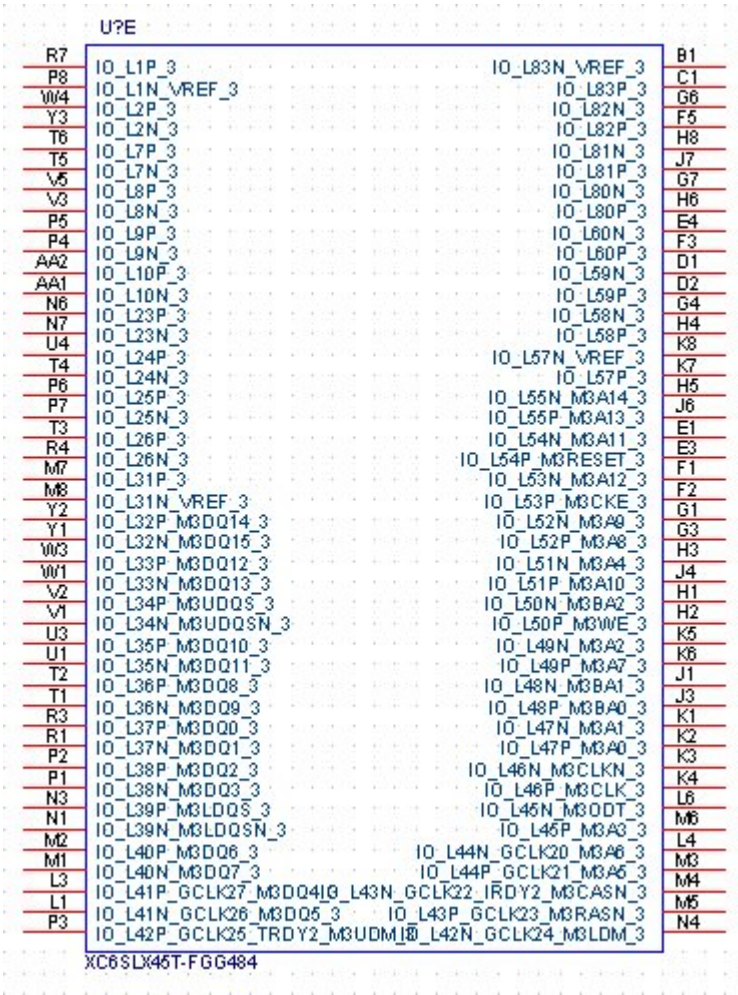
3. I/O Bank 1



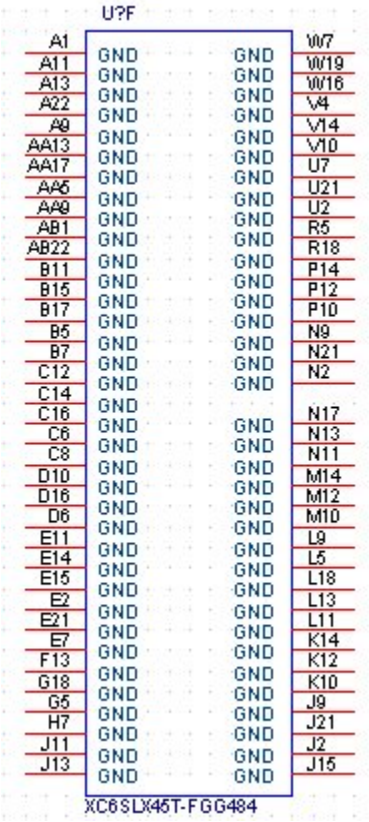
4. I/O Bank2 (Contains the Programming Interface)



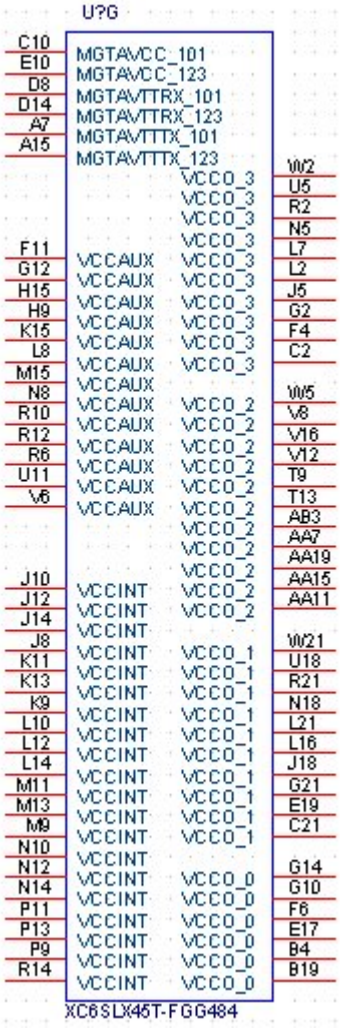
5. I/O Bank 3



6. GND



7. Power



Notes:

1. The dedicated pins DONE_2 and PROGRAM_B are powered by Bank2.
2. The JTAG pins and SUSPEND are powered by VCCAUX.
3. When SUSPEND is not used, connect this pin to GND.
4. CMPCS_B_2 –Reserved Input. Connect high or leave unconnected.
5. The following parts in this package have similar but not identical pinout: LX25T, LX45T, LX75T, LX100T and LX150T. If migration between different component densities is desired, please pay attention to the NC pins on each of the devices that are targeted for implementation. For details please check the UG385, "Spartan 6 Packaging and Pinouts" User Guide that can be found at: http://www.xilinx.com/support/documentation/user_guides/ug385.pdf

Document Revision History

	Revision	Date	By	Comments
1	1.00	Mar 15, 2010	LD	Initial Release –Uses Xilinx Pinout ASCII File -02/22/2010. Check the Xilinx website for updates.